

EUROPEAN PATENT OFFICE

Patent Abstracts of Japan

PUBLICATION NUMBER : 2000059238
PUBLICATION DATE : 25-02-00

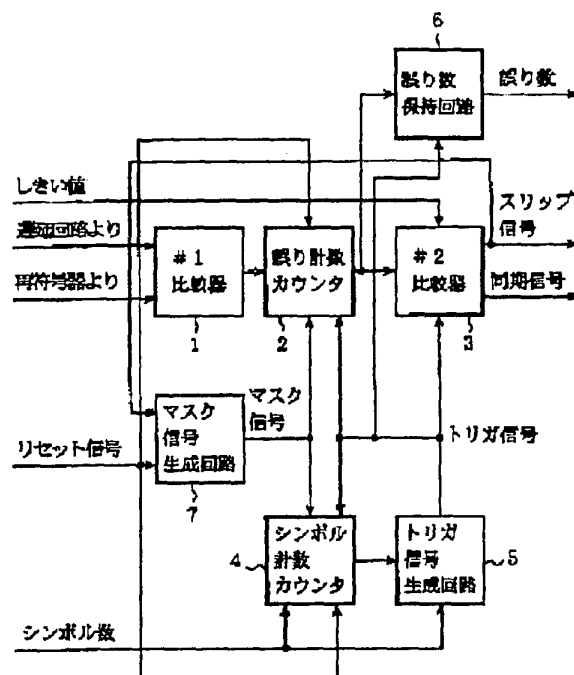
APPLICATION DATE : 04-08-98
APPLICATION NUMBER : 10220725

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INT.CL. : H03M 13/23

TITLE : CODE SYNCHRONIZATION
DISCRIMINATING CIRCUIT OF
VITERBI DECODER



ABSTRACT : PROBLEM TO BE SOLVED: To realize the code synchronization discriminating circuit of a viterbi decoder, which allows a user to easily set a threshold value and the number of symbols without mistakes and can reduce power consumption.

SOLUTION: A trigger signal generating circuit 5 outputs a trigger signal when a symbol counting counter 4 counts the number of symbols set from outside. Then, when the trigger signal is inputted, a #2 comparator 3 outputs a synchronization signal or a slip signal to a mask signal generation circuit 7 based on a comparing result between the number of errors counted by an error counting counter 2 and a threshold value set from outside to allow it to generate a mask signal for stopping the operation of the error counting counter and the symbol counting counter in a period for which a code that is not suitable as a comparing value is inputted.

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